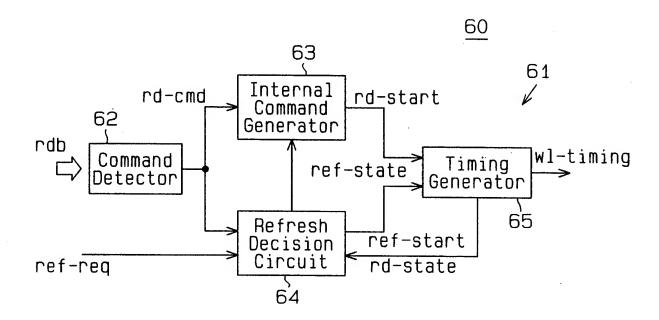
Title: Semiconductor Memory Device and Control method Therefor

Inventor's Name: NAKAGAWA

Application No.: NEW Docket No.: 108075-00114

Fig.1 (Prior Art)

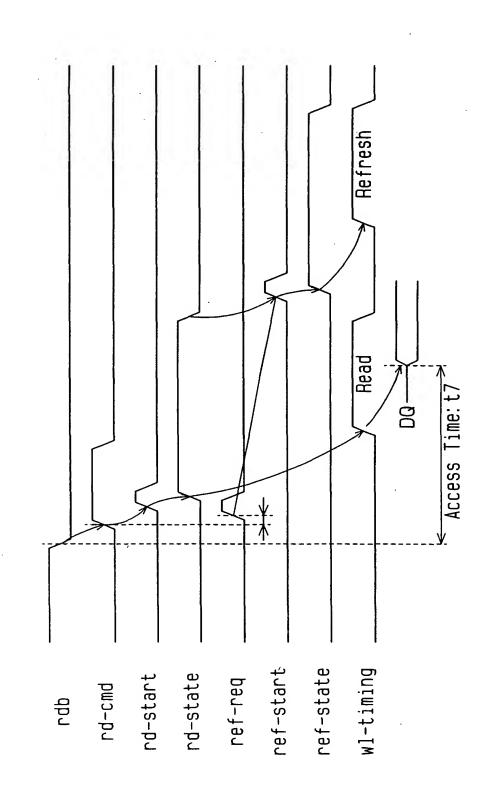


Control method Therefor

Inventor's Name: NAKAGAWA

Application No.: NEW Docket No.: 108075-00114

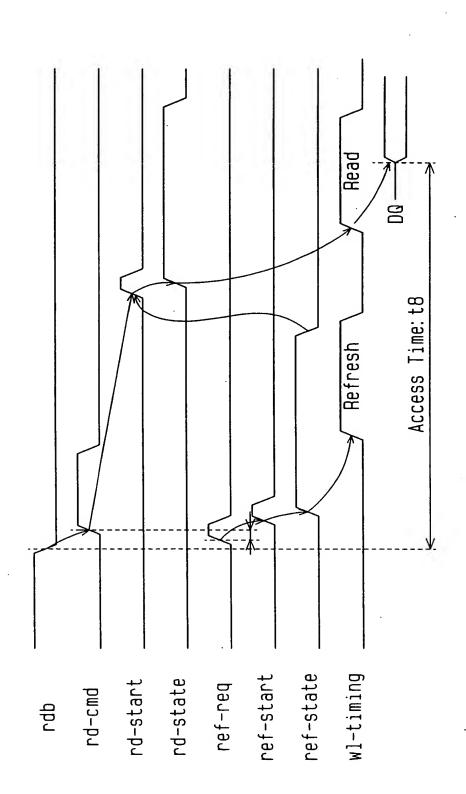
Fig. 2 (Prior Art)

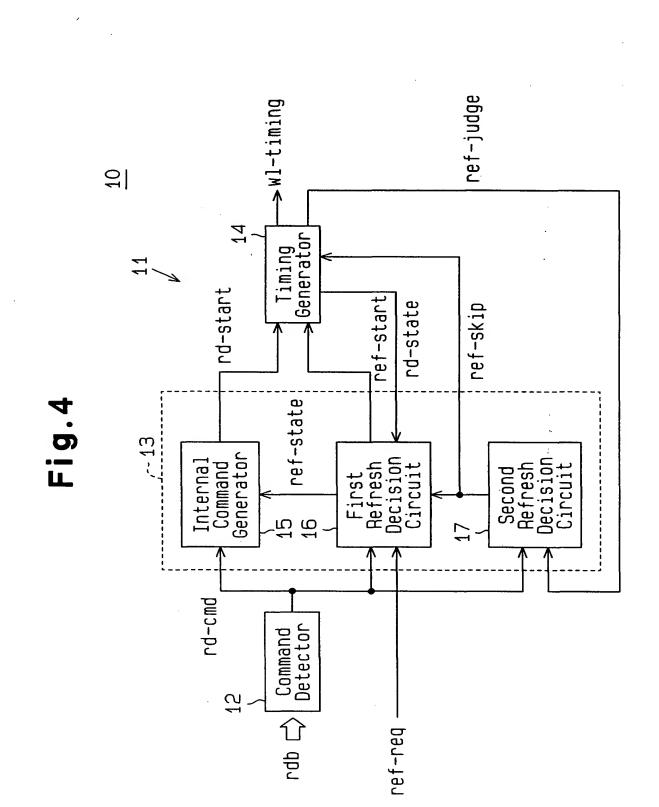


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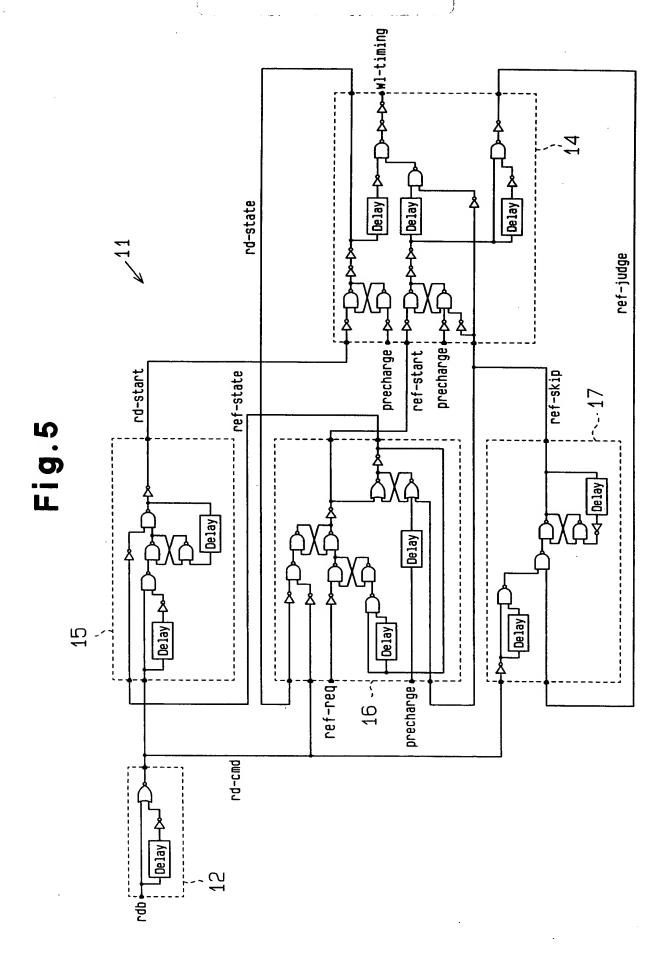
Fig. 3 (Prior Art



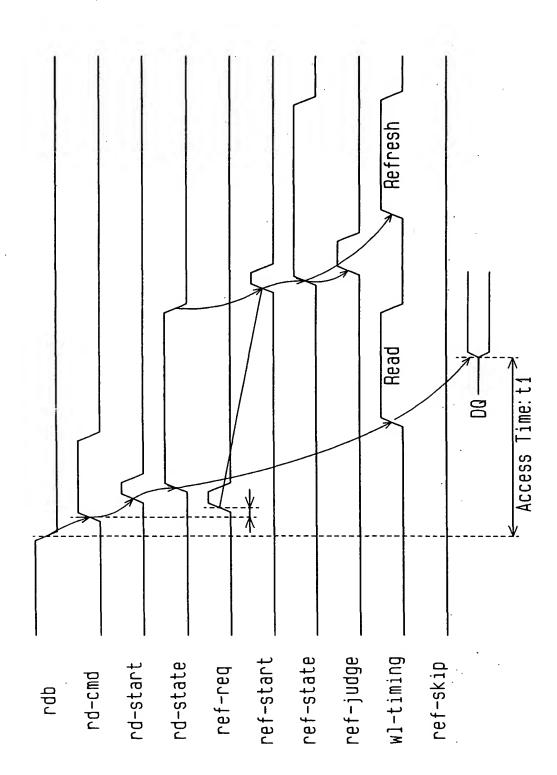


Control method Therefor
Inventor's Name: NAKAGAWA
Application No.: NEW
Docket No.: 108075-00114

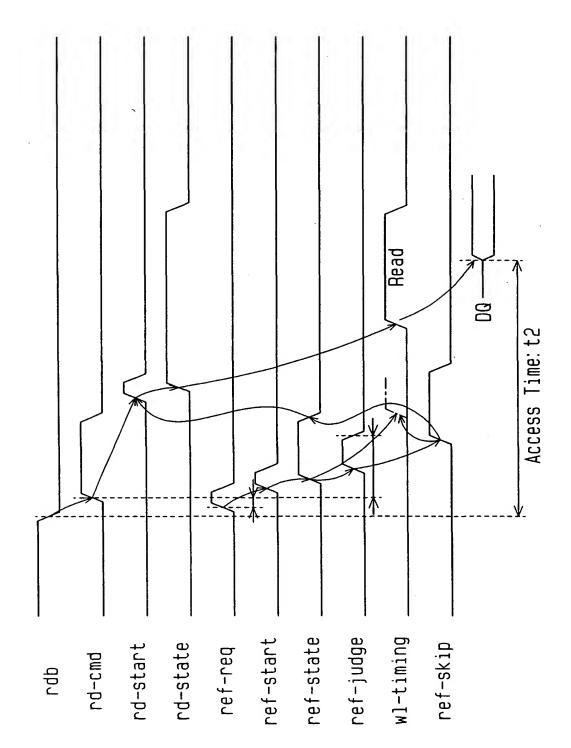
Title: Semiconductor Memory Device and Control method Therefor Inventor's Name: NAKAGAWA Application No.: NEW Docket No.: 108075-00114



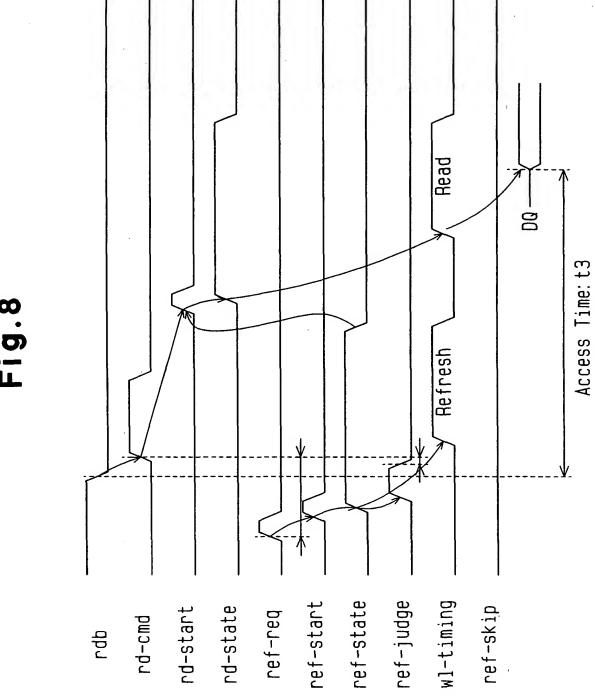
Title: Semiconductor Memory Device and Control method Therefor Inventor's Name: NAKAGAWA Application No.: NAKAGAWA Docket No.: 108075-00114



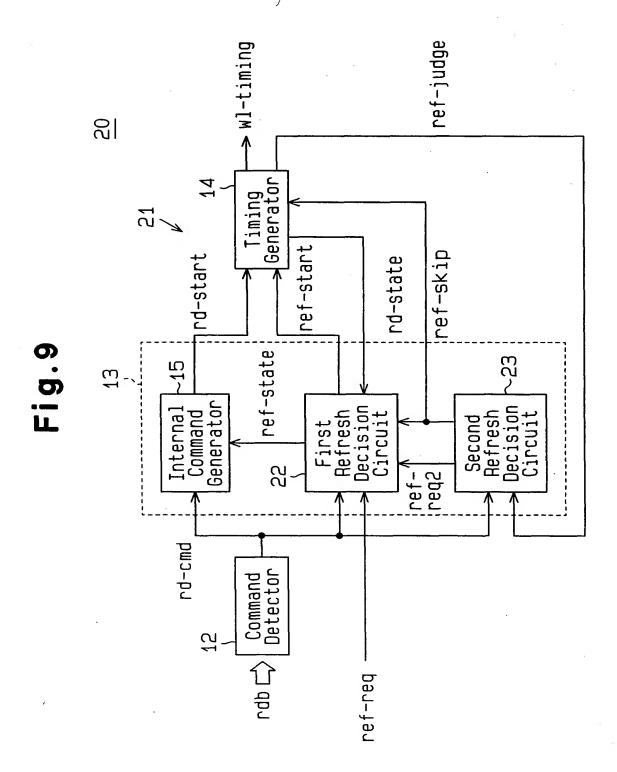
Control method Therefor Inventor's Name: NAKAGAWA Application No.: NEW Docket No.: 108075-00114



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Application No.: NEW
Docket No.: 108075-00114



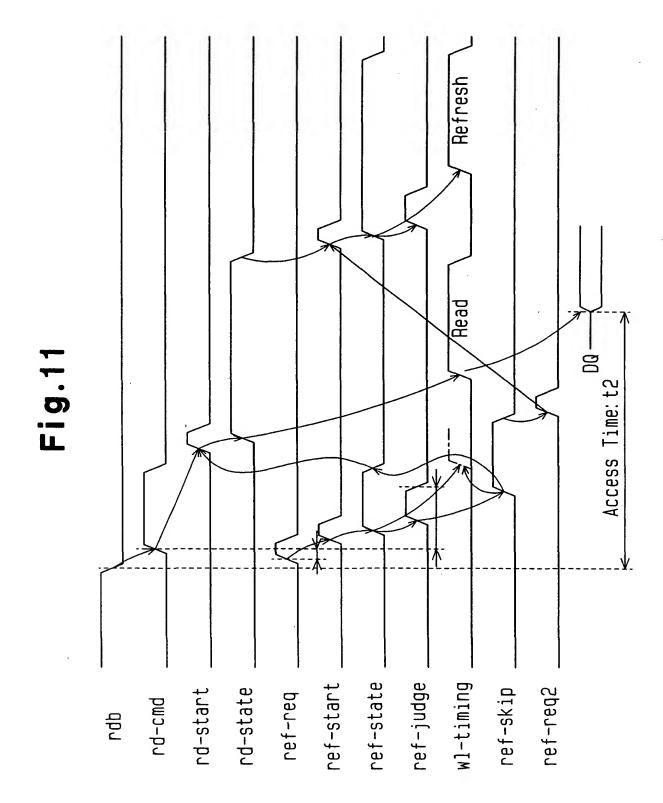
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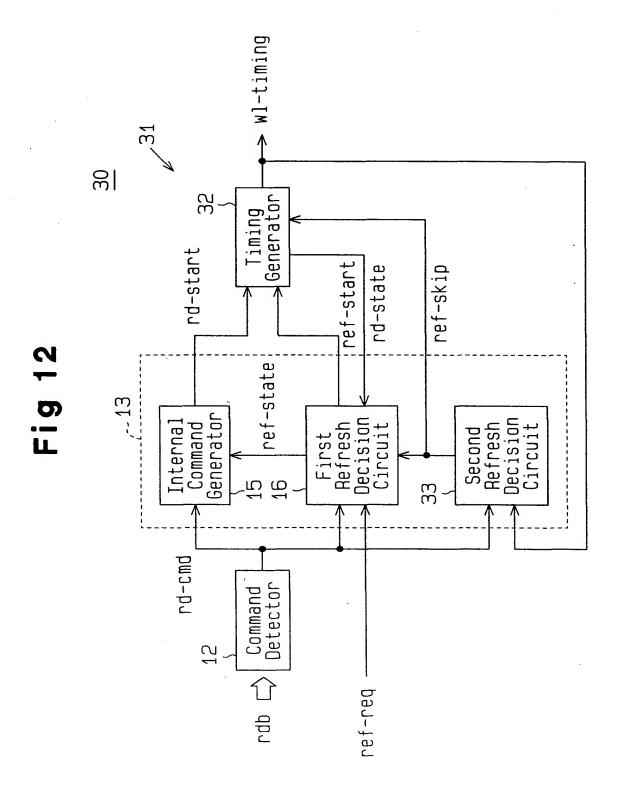
Title: Semiconductor Memory Device and Control method Therefor Inventor's Name: NAKAGAWA Application No.: NEW Docket No.: 108075-00114 De lay \rightarrow De lay T Delay rd-state /21 ref-req2 ...23 precharge, precharge, ref-start ref-state ref-skip rd-start Fig.10 De lay Delay 15 22.-1 ref-req precharge —

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Title: Semiconductor Memory Device and Control method Therefor Inventor's Name: NAKAGAWA Application No.: NEW Docket No.: 108075-00114 wl-timing rd-state precharge precharge ref-start ref-state ref-skip rd-start Fig.13 Delay 15 16 -′ 33... ref-reg precharge

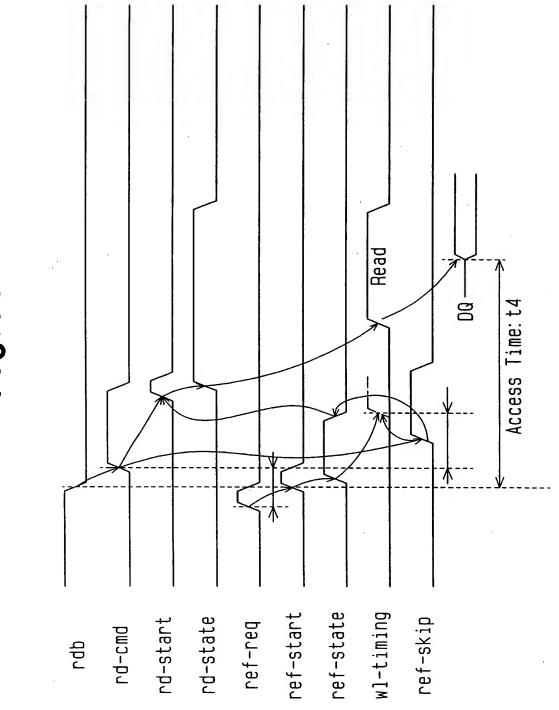
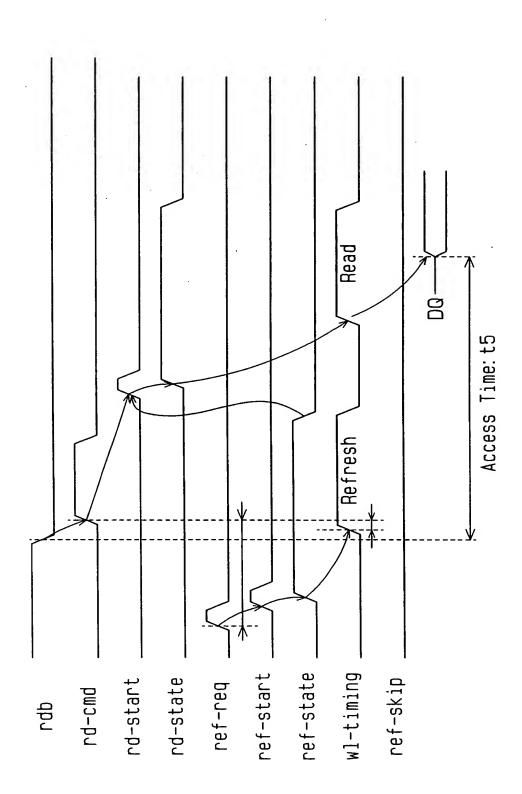


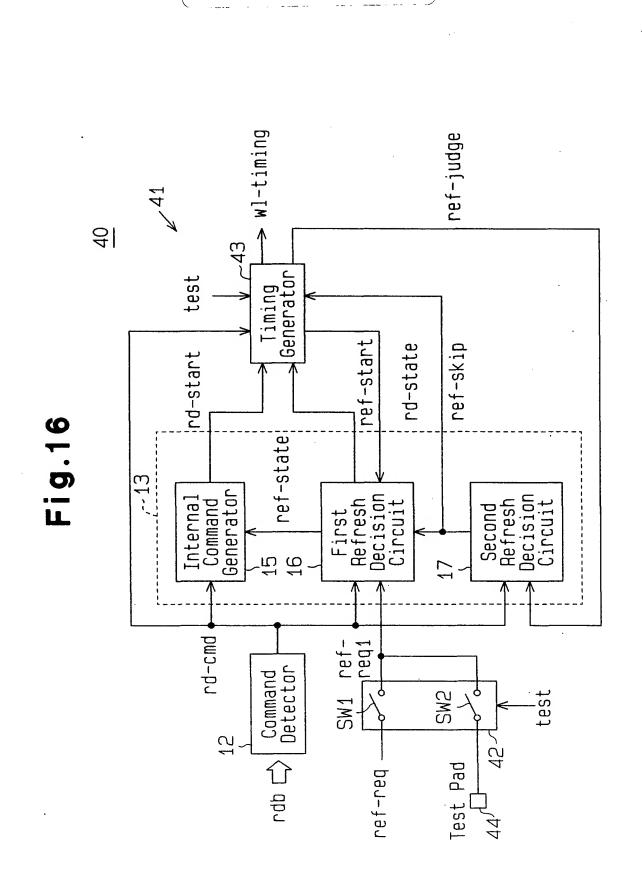
Fig. 14

Control method Therefor

Inventor's Name: NAKAGAWA







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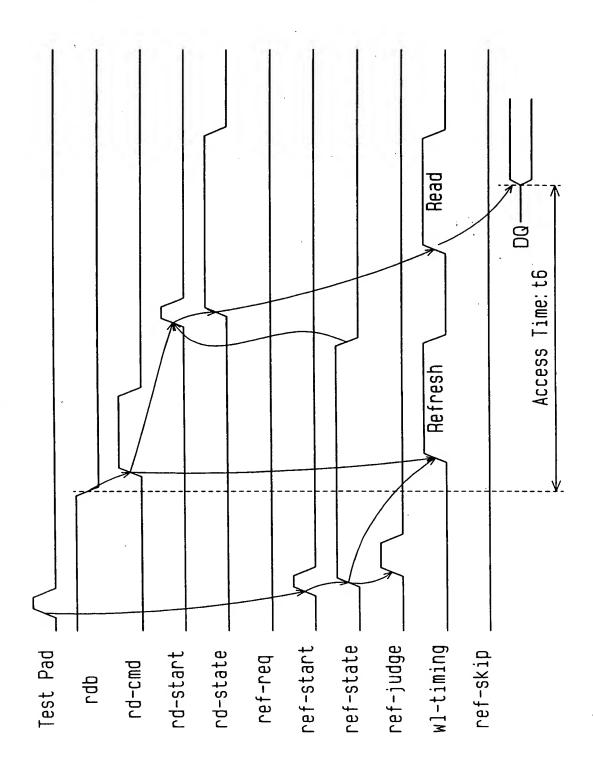
Inventor's Name: NAKAGAWA Application No.: NEW Docket No.: 108075-00114 → W1-timing De lay rd-state testA precharge precharge ref-start ref-state rd-start ref-skip Fig.17 Delay 15... precharge 💺 ref-req1 16-/ 12

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Inventor's Name: NAKAGAWA





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Fig.19

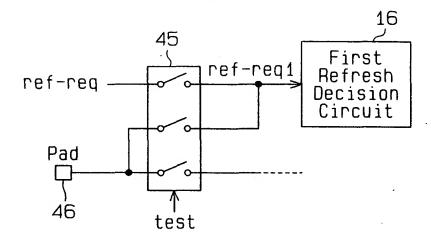


Fig. 20

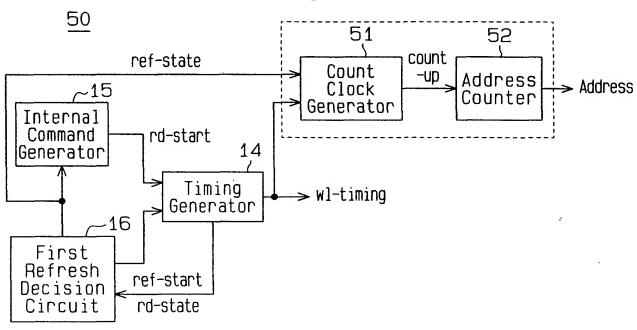


Fig. 21

